

WEST

Generate Collection

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L13: Entry 29 of 37

File: USPT

Dec 18, 1990

DOCUMENT-IDENTIFIER: US 4979039 A

TITLE: Method and apparatus for vector quantization by hashing

Detailed Description Paragraph Right (47):

The address of the one code word having the least distortion with respect to the compared signal vector 28 is used as the code 34 for the hashed signal vector 28 and is placed by the microprocessor 152 in a compressed image file, which may be one of the two direct random access memory banks 140 or 148. The process is repeated by the microprocessor 152 for each signal vector 28 partitioned from each of the component pixel planes 222, 228 and 230 until all signal vectors of the Y and reduced I and Q planes 222, 228 and 230 have been quantized and a file of an entire compressed image is created containing only code word addresses or codes 34.